

2FHD0220 Data Sheet

Abstract

2FHD0220 series driver is a high-performance, dual-channel SiC gate driver developed in-house by Firstack based on ASIC platform, supporting single modules up to 2000V in the SiC 62mm package with configurable gate voltage. The driver supports up to 2W output per channel and is suitable for high power density application by using SiC module.

Core Features:

- Support 2W per channel
- Support up to 2000V SiC module
- Short-circuit protection(soft shut down)
- Miller clamping
- Configurable drive voltage
- Intelligent fault feedback
- UVLO

Typical Application:

- ESS
- Rail
- Motor drives

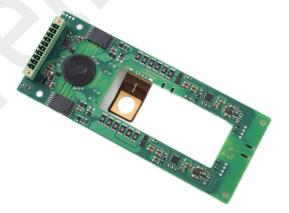


Fig. 1 2FHD0220



Functional Block Diagram

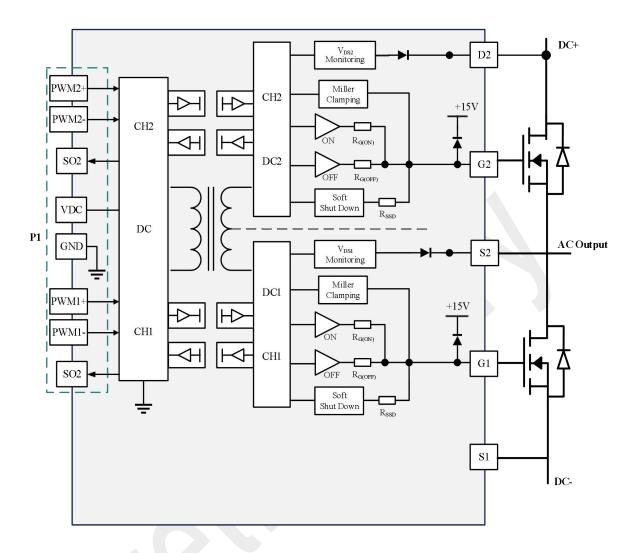
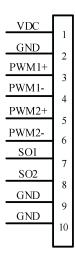


Fig. 2 Functional block diagram



Connector Interface Definition



P1 terminal pin designation

Pin	Definition	Function
1	V_{DC}	Power supply input
2	GND	Primary side ground
3	PWM1+	Positive signal input 1
4	PWM1+	Negative signal input 1
5	PWM2+	Positive signal input 2
6	PWM2+	Negative signal input 2
7	SO1	Status output channel 1
8	SO2	Signal input channel 2
9	GND	Primary side ground
10	GND	Primary side ground



Technical Parameters

Absolute Maximum Ratings

Parameter	Remarks	Min	Max	Unit
Supply voltage V _{DC}	V _{DC} to GND	0	15.5	V
Gate peak current	@85°C	-20	20	A
Output power per channel	@85°C		2	W
Test voltage (50Hz/1min)	Primary to secondary side	6000		V_{RMS}
Operating temperature		-40	85	°C
Storage temperature		-40	85	°C

Recommended Operating Conditions

Parameter	Remarks	Min	Тур	Max	Unit
Supply voltage V_{DC}	V _{DC} to GND	14.5	15	15.5	V
Supply current I _{DC}	Without load		0.12		A
Primary side undervoltage threshold	Supply voltage		12		V
Secondary side undervoltage threshold	Secondary side positive voltage		12.5		V

Gate Driver Parameters

Output voltage	Remarks	Min	Тур	Max	Unit
Total gate voltage	Turn on (ON) - off (OFF) voltage	19.5	20	24.5	V
$\begin{array}{ccc} \text{Gate} & \text{positive} & \text{voltage} \\ V_{\text{GSon}} & & \end{array}$	Turn on (ON)	14.5	15	19.5	V
$\begin{array}{ccc} \text{Gate} & \text{negative} & \text{voltage} \\ \hline V_{\text{GSoff}} & & & \end{array}$	Turn off (OFF)	-9.5	-5	-0.5	V

Logic Inputs & Outputs

Parameter	Remarks	Min	Тур	Max	Unit
Input signal INx	INx to GND	14.5	15	15.5	V
Input impedance			100		$k\Omega$



Turn-on threshold	V(INx)	9.2		V	
Turn-off threshold	V(INx)		3.2	V	
Fault output SOx	Protection state @Io<10mA		0.35	V	
Direct mode		Set by software, no configuration required			
MOD mode	Half-bridge mode	Set by software, no configuration required			

Short-circuit protection

Parameter	Remarks	Min	Тур	Max	Unit
V _{DS} monitoring threshold	Short-circuit protection monitoring	Configurable	11	Configurable	V
Pagnanga tima	CH1, Note 1	Configurable	2	Configurable	μs
Response time	CH2, Note 1	Configurable	2	Configurable	μs
Soft shut down time	Soft shut down action time	Configurable	4.16	Configurable	μs

Miller Clamping

Parameter	Remarks	Min	Тур	Max	Unit
Time from drive signal turn-off to clamp turn-on		Configurable	1.04	Configurable	μs
Time from clamp turn-off to drive signal turn-on			700		ns
Clamp voltage			V_{SS}		

Timing Characteristics

Parameter	Remarks	Min	Тур	Max	Unit
Turn-on delay	Note 2		1.2		ns
Turn-off delay	Note 3		1.3		ns
Rise time	Note 4		12		ns
Fall time	Note 5		12		ns
Fault blocking time			80		μs



Fault return time Note 6 10 ms

Electrical Isolation

Parameter	Remarks	Min	Typ	Max	Unit
C. I'.	Primary to secondary side, Note 8		8		mm
Creepage distance	Secondary to secondary side, Note 8		6.5		mm
Classes and distance	Primary to secondary side		8		mm
Clearance distance	Secondary to secondary side		5		mm

Unless otherwise specified, all data are based on tests at $\pm 25^{\circ}$ C ambient temperature and $V_{DC}=15V$.

Note:

- 1. Response time: the time from the occurrence of the fault to the start of soft shut down;
- 2. Turn-on delay: the time required to transmit the rising edge of the PWM signal input from the primary side to the rising edge of the secondary side of the gate driver;
- 3. Turn-off delay: the time required to transmit the falling edge of the PWM signal input from the primary side to the falling edge of the secondary side of the gate driver;
- 4. Rise time: the amount of time from 10% of the gate turn-off voltage to 90% of the gate turn-on voltage;
- 5. Fall time: the amount of time from 90% of the gate turn-on voltage to 10% of the gate turn-off voltage;
- 6. Fault return time: short-circuit 10ms, secondary side undervoltage 20ms, primary side undervoltage 40ms;



Updates

Date	Description	Version	
2025.04.27	Preliminary	V0.1	

Ordering Information

2FHD0220 can support different models of package modules from multiple manufacturers. When purchasing, please add the module model number after the driver model number so that we can provide the driver that best meets your needs.

Part Number	Voltage	R _{GON} (Ω)	$R_{ ext{GOFF}}(\Omega)$	R _{SSD} (Ω)	Coating
2FHD0220C17A1-S0001	2000V	10//10//10	10//10//10	47	W/O



Technical Support

Firstack's professional team will provide you with business consultation, technical support, product

selection, price, lead time and other related information, and guarantee to answer your questions within 48

hours.

Legal Disclaimer

The instruction manual provides a detailed description of the product but does not commit to providing

specific parameters regarding the delivery, performance, or applicability of the product. This document

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